

# BEST AVAILABLE COPY

FROM: ROGITZ 619 338 8078

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1. (currently amended) A HDD comprising:

at least one write channel including at least one write gate; and  
control circuitry controlling the write gate using write control bits to selectively enable writing data bits associated with a servo pattern onto at least one disk, wherein the control circuitry uses two bits of a ten bit parallel bus as write control bits to indicate whether the write gate should enable writing one or more of the remaining eight bits of the bus to disk.

2. (original) The HDD of Claim 1, wherein the control circuitry writes a servo pattern after the HDD has been sealed.

3. (original) The HDD of Claim 1, wherein the write channel is used during operation to write user data to the disk.

4. (canceled).

5. (currently amended) ~~The HDD of Claim 1,~~ A HDD comprising:

at least one write channel including at least one write gate; and  
control circuitry controlling the write gate using write control bits to selectively enable writing data bits associated with a servo pattern onto at least one disk, wherein the control circuitry uses four bits of an eight bit parallel bus as write control bits to indicate whether the write gate should enable writing one or more of the remaining four bits of the bus to disk.

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6. (original) The HDD of Claim 3, wherein the control circuitry uses a write delay to a next timing mark based on detecting a current timing mark, the time delay including a clock cycle component and a clock phase component, the write channel using the write delay to write the next timing mark and associated portions of the servo pattern to disk.

7-19 (canceled).

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